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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : McCall et al.

Art Unit : 2181

Serial No. : 09/666,489

Examiner : Glen Allen Auve

Filed : September 18, 2000

Assignee : Intel Corporation

Title : BUFFERING DATA TRANSFER BETWEEN A CHIPSET AND MEMORY MODULES

RECEIVED

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAY 07 2004

Technology Center 2100

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed, i.e., a Response, a Terminal Disclaimer, and a Declaration Under 37 C.F.R. § 1.131. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	21	-	23	=	0	\$0
Independent	4	-	4	=	0	\$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 3 month extension of time.

\$950

TOTAL FEE DUE

\$950

A check for \$950 is attached.

Respectfully submitted,

Date: April 30, 2004

Scott C. Harris
Reg. No. 32,030

Attorneys for Intel Corporation

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit April 30, 2004

Signature

Typed or Printed Name of Person Signing Certificate Carroll Allman